The joint 4rd International Workshop on CPAchecker (CPA'19) and 9th Linux Driver Verification (LDV) Workshop
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Static Verification Results Visualization in the Context of SV-COMP

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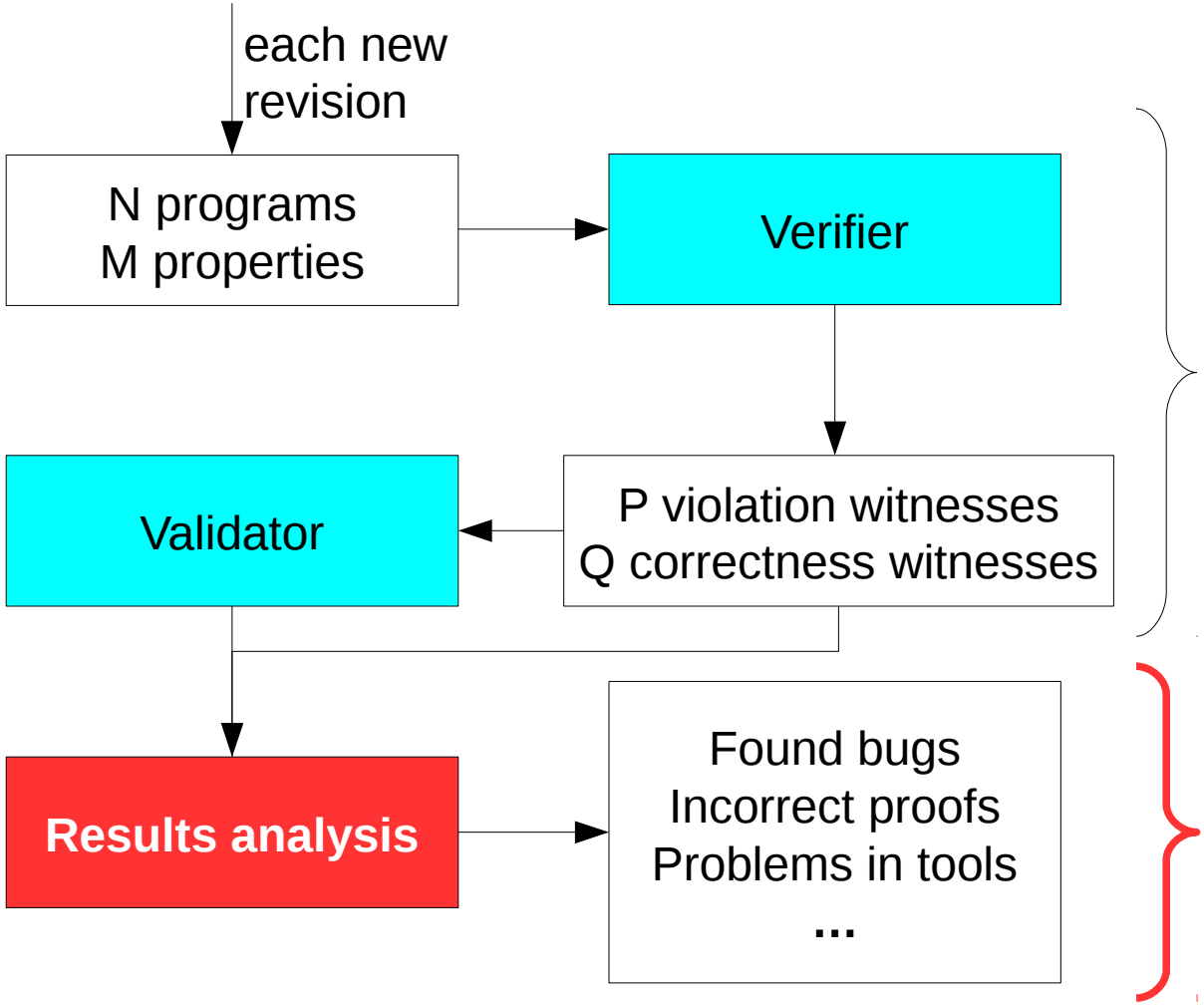
Static Verification State of the Art

- More than 31 tools*
- Improvements in effectiveness and efficiency*
- Validation of verification results*
  - Both property violations and correctness proofs*
- Different properties*
  - Potential for extensions (e.g., property automata)
- Verification of C and Java programs*

What about results analysis?

* D. Beyer. *Automatic Verification of C and Java Programs: SV-COMP 2019*.

Verification of Industry System



Automatic step:
required resources **can** be minimized with modern approaches and cloud technologies

Manual step:
required resources are **harder** to reduce (user experience), but cost much more

Related Work

- BenchExec* table-generator
  - Score (based on tasks definition)
  - Plots with consumed resources
  - Comparison tables
  - Witness validation results
  - Witnesses are not visualized
- LDV Tools (Klever)**
  - Preset environment models for Linux/BusyBox/etc.
  - Violation witnesses visualization
    - Specific format

Presented in machine-readable format

Not supported by SV-COMP tools

➡ **Cannot** visualize generic witness from SV-COMP tools

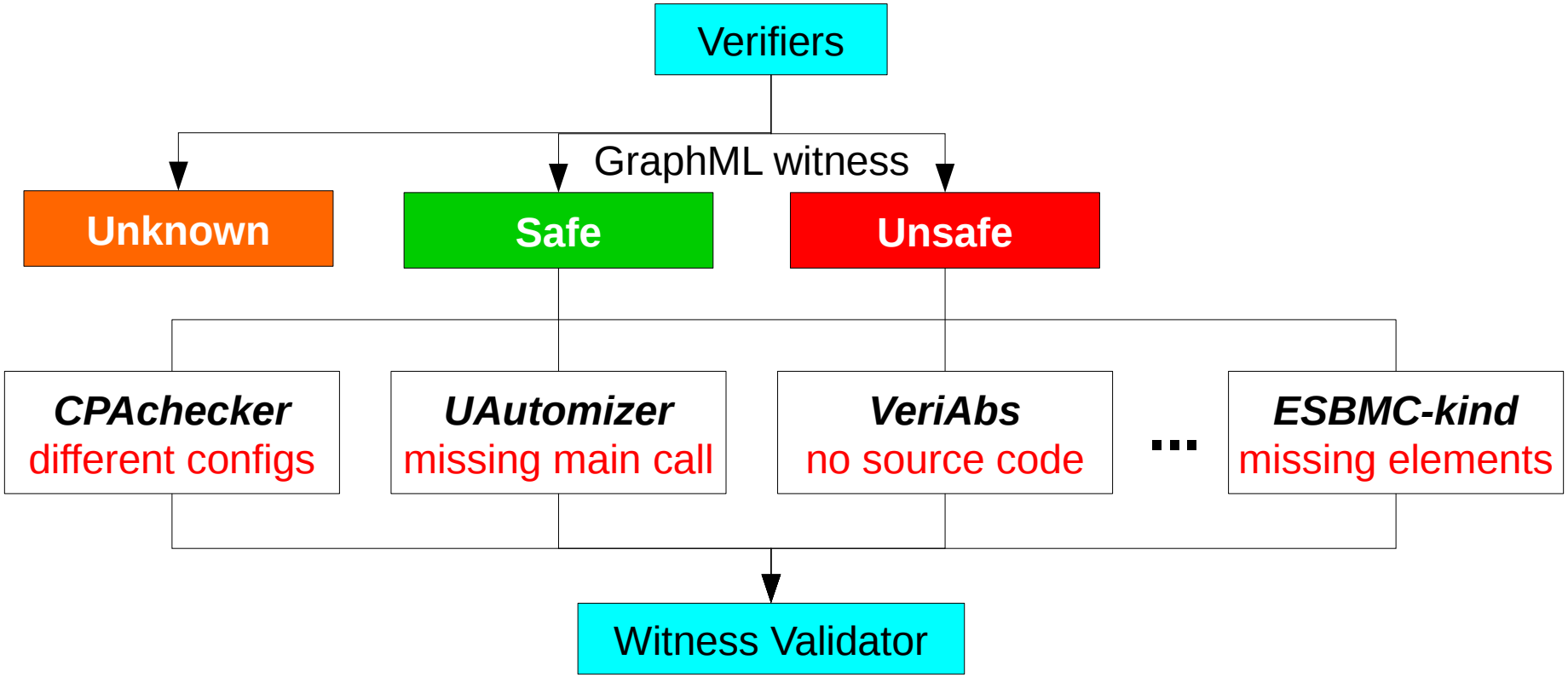
* https://github.com/sosy-lab/benchexec

** https://forge.ispras.ru/projects/klever

Suggested Solutions

- Witness Visualizer (user-friendly witnesses)
  - Helps to locate bugs for the users
  - Helps to reveal problems in tools
- Correctness witnesses visualization (idea)
  - Shows main proof hints (for developers)
  - Presents source code coverage (for users)
- Benchmark Visualizer (continuous verification)
  - Visualizes BenchExec results
  - Groups witnesses for each benchmark

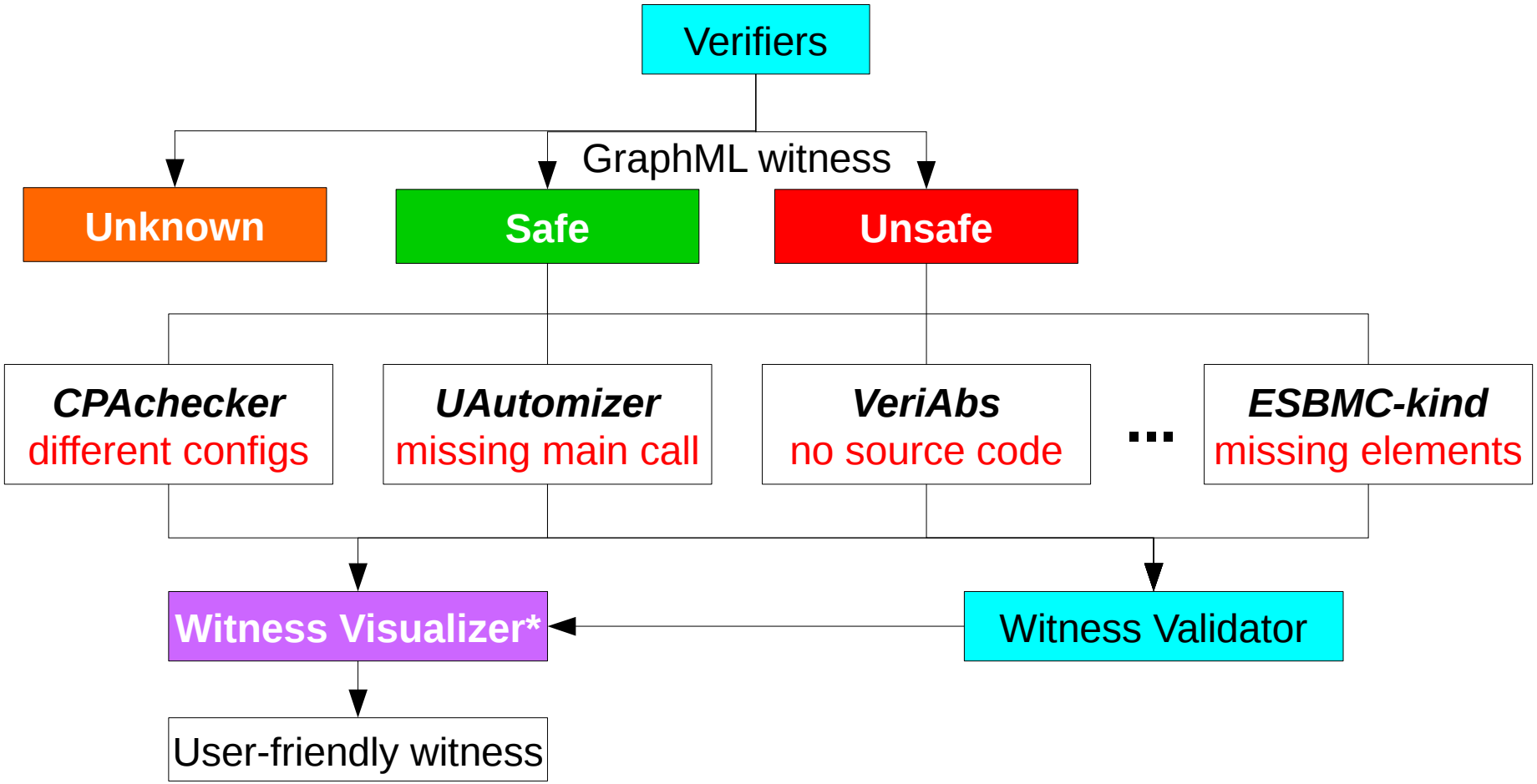
Common Witness Format*



- Machine-readable format
- There are still differences among tools

* D. Beyer, M. Dangl, D. Dietsch, M. Heizmann, A. Stahlbauer. *Witness validation and stepwise testification across software verifiers*. ACM, 2015.

Witness Visualizer



* https://github.com/ispras/cv

Requirements to the Witness Visualizer

- Fault tolerance (to the missing elements)
  - Support common witness format (GraphML)
- Quality control (for developers)
  - Provide feedback on the missing elements
- Support violation hints
  - Helps with large witnesses
- Provide operations with witnesses
  - Comparison
- Support both violation and correctness types

Fault Tolerance

- Cannot be tolerated
  - Parsing failures (wrong format)
  - Empty witnesses
- Restorable missing elements
  - Source code (program file + line/offset)
  - Entry point (based on property description)
  - Property violation (last edge)
- Elements, which cannot be restored
  - Call stack
  - Assumptions/controls

Quality Control

- Provide useful feedback to the developers
  - Source files do not exist
  - Call stack is missing
  - Conditions are missing
  - Entry point is missing
  - Produced warnings during visualization

1) No call stack (enterFunction tag)
2) No conditions (control tag)

Warning: some elements are missing

Violation Hints

- Core elements, which describe the given violation
- Reason – visualize large witnesses
  - Highlight violation hints
    - With call stack, source code link, thread id, etc.
  - Hide other elements
- Violation hints extraction
  - From witnesses (“note”, “warning”)

    <data key="note">Acquire mutex_lock</data>*
  - From property
  - From source code**

```
OBSERVER AUTOMATON A
...
  MATCH {func($?)} -> ...
...
```

* Example is based on witnesses from *CPA-Lockator* tool.

** Based on model comments (applied in LDV Tools).

Violation Hints Usage Example

Initial witness

```
main()
  void *x = NULL;
  int flags;
  int size;
  int i = 0;
  f1(i)
    assume(i < 10)
    f2(i)
      f3(i)
    i := i + 1
    ...
  x = alloc(size, flags)
    return NULL
  ...
  free(x)
```

Processed witness

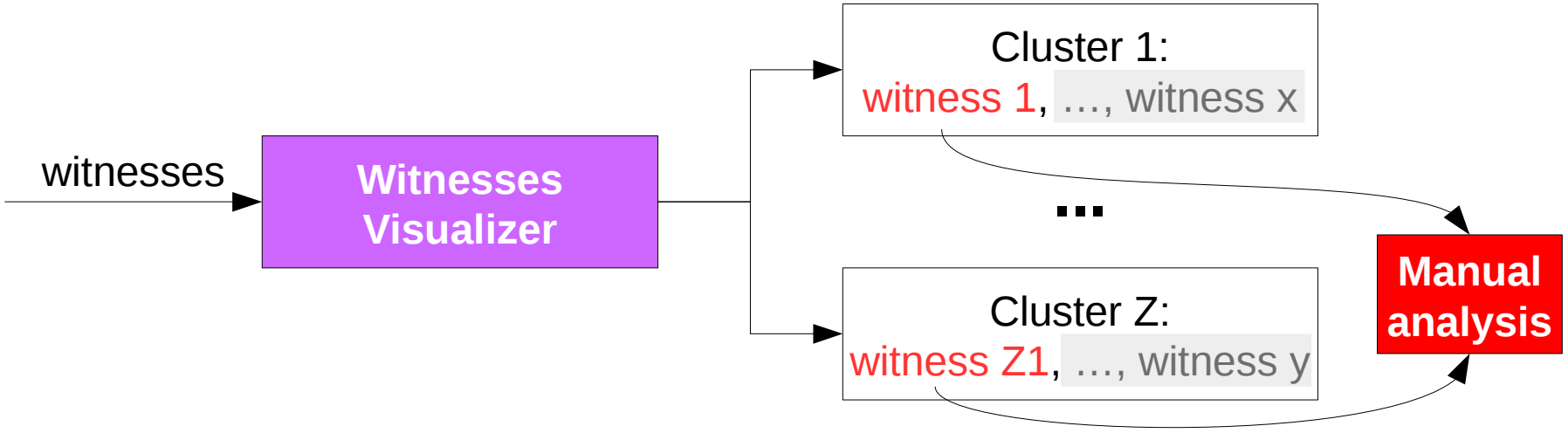
Hidden elements

```
main()
  void *x = NULL;
  int flags;
  int size;
  int i = 0;
  f1(i)
    assume(i < 10)
    f2(i)
      f3(i)
    i := i + 1
    ...
  Allocate memory for x
    Failed to allocate x
  ...
  Null ptr dereference on x
```

Violation hints

Operations with Witnesses

- Witnesses comparison
  - Distinguish different witnesses (error paths)
  - Filter several witnesses*
  - Can be done for validated witnesses only



* For example, SV-COMP tool *CPA-Lockator* can produce several witnesses for concurrency properties.

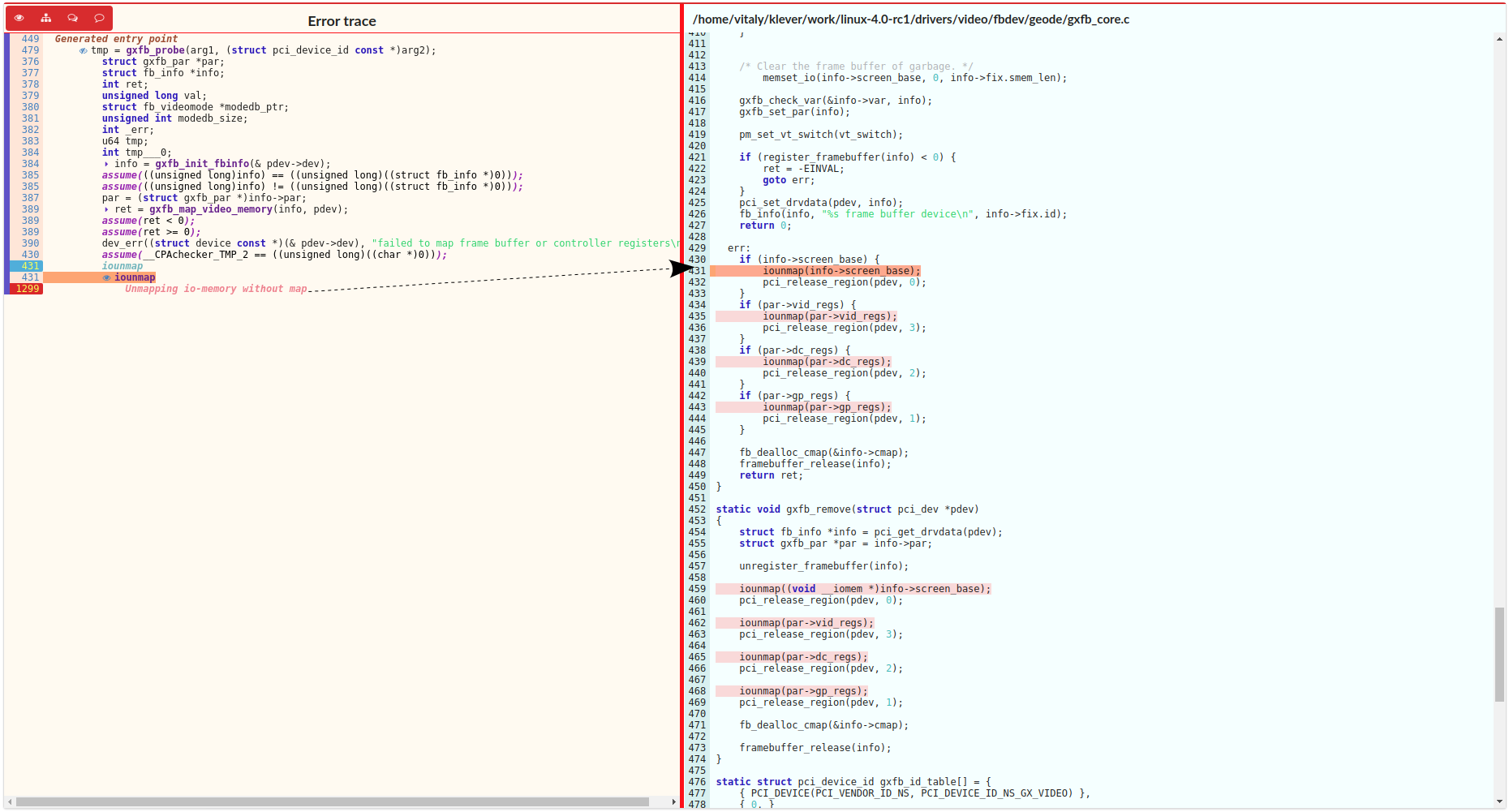
SV-COMP Tools Violation Witnesses

SV-COMP Tool	Witness elements			Source code				Violation Hints
	Call stack	Entry point	Assumptions/controls	String	Offset	Line number	File name	
2LS	-	-	+	-	-	+	+	-
AProVE	-	+	-	+	-	+	+	-
CBMC	-	-	+	-	-	+	+	-
CBMC-Path	-	-	+	-	-	+	+	-
CPA-BAM-BnB	+	+	+	+/-	+	+	+	+/-
CPA-Lockator	+	+	+	+/-	+	+	+	+/-
CPA-Seq	+	+	+	+/-	+	+	+	+/-
DepthK	-	+	+	-	-	+	+	-
DIVINE-explicit	+	+	-	-	-	+	+	-
DIVINE-SMT	+	+	-	-	-	+	+	-
ESBMC-kind	-	+	+	-	-	+	+	-
Lazy-CSeq	+	+	+	-	-	+	+	-
Map2Check	-	-	+	-	-	+	+	-
PeSCo	+	+	+	+/-	+	+	+	+/-
Pinaka	-	-	+	-	-	+	+	-
PredatorHP	-	-	-	-	-	+	+	-
Skink	-	-	+	-	-	+	+	-
SMACK	-	-	+	-	-	+	+	-
Symbiotic	-	-	+	-	-	+	+	-
UAutomizer	+	-	+	+	-	+	+	-
UKojak	+	-	+	+	-	+	+	-
UTaipan	+	-	+	+	-	+	+	-
VeriAbs	+	+	+	-	+	+	+	-
VeriFuzz	-	-	+	-	-	+	+	-
VIAP	+	+	-	+	-	+	+	-
Yogar-CBMC	+	+	-	-	-	+	+	-
Yogar-CBMC-Parallel	+	+	-	-	-	+	+	-

* 4 verifiers for Java programs were excluded from this comparison, because they do not produce witnesses.

Example of a Witness with Violation Hints

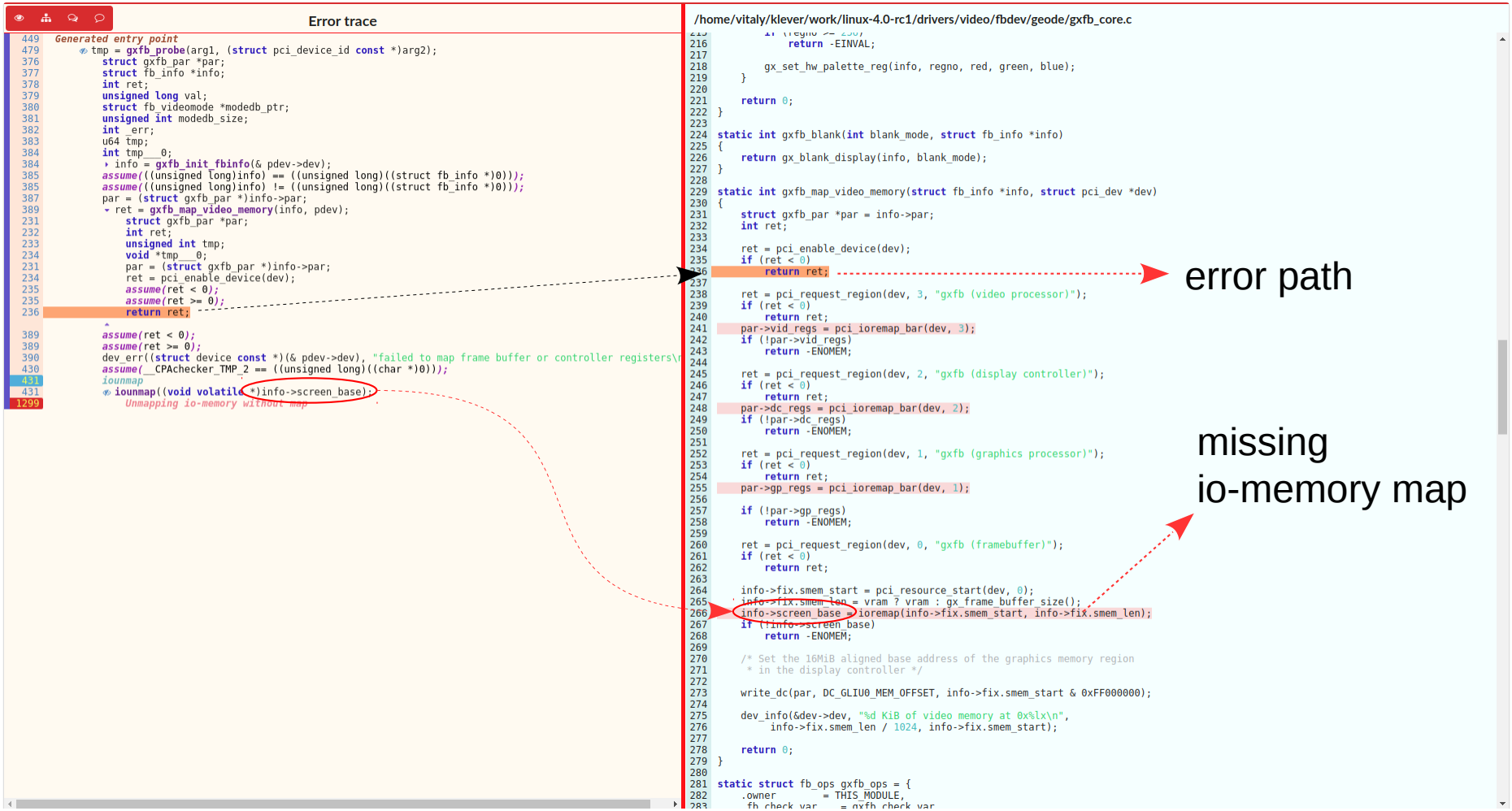
- Input/output memory map operations: `ioremap`, `pci_ioremap_bar`, ...
- Input/output memory unmap operation: `iounmap`



* Violation witness visualization is based on LDV Tools (Klever): https://forge.ispras.ru/projects/klever

Example of a Witness with Violation Hints

- Input/output memory map operations: `ioremap`, `pci_ioremap_bar`, ...
- Input/output memory unmap operation: `iounmap`



Example of a Witness with Missing Elements

Error trace

```
  0  entry point
3964      tmp___0 = 0;
6450      tmp___0 = 9215;
6451      ldv_arg3 = 9215;
3964      tmp___0 = -1;
3964      tmp___0 = 0;
6465      ldv_arg5 = 4294967295;
6473      tmp___7 = 1;
6484      tmp___8 = 1;
6242      ldv_retval_0 = 0;
3964      tmp___0 = -1;
3964      tmp___0 = -1;
3964      tmp___0 = -1;
6473      tmp___7 = 3;
6717      tmp___10 = 0;
4281      return (struct max3421_hcd *)(& hcd->hcd_priv);
4281      return (struct max3421_hcd *)(& hcd->hcd_priv);
6117      max3421_hcd_list = max3421_hcd;
6185      ldv_retval_2 = -12;
6473      tmp___7 = 2;
6527      tmp___9 = 10;
5720      max3421_ep = (struct max3421_ep *)(urb->ep)->hcpriv;
6794      flags = 4294967295;
4008      Property violation
```

/home/vitaly/ose/bl/esbmc/sv-benchmarks/c/ldv-linux-3.16-rc1/43_2a_bitvector_linux-3.16-rc1.tar.xz-43_2a-drivers--usb--host--max3421-hcd.ko-entry_point_false-unreach-call.cil.out.c

```
6768 }
6769 __inline static void *kmalloc(size_t size , gfp_t flags )
6770 {
6771
6772
6773   {
6774   ldv_check_alloc_flags(flags);
6775   ldv_kmalloc_12(size, flags);
6776   return ((void *)0);
6777 }
6778 }
6779 void *ldv_kmem_cache_alloc_16(struct kmem_cache *ldv_func_arg1 , gfp_t flags )
6780 {
6781
6782
6783   {
6784   ldv_check_alloc_flags(flags);
6785   kmem_cache_alloc(ldv_func_arg1, flags);
6786   return ((void *)0);
6787 }
6788 }
6789 __inline static void *kzalloc(size_t size , gfp_t flags )
6790 {
6791
6792
6793   {
6794   ldv_check_alloc_flags(flags);
6795   return ((void *)0);
6796 }
6797 }
6798 __inline static int ldv_request_irq_19(unsigned int irq , irqreturn_t (*handler)(int ,
6799                                                                                  void * ) ,
6800                                        unsigned long flags , char const   *name ,
6801                                        void *dev )
6802 {
6803   ldv_func_ret_type___2 ldv_func_res ;
6804   int tmp ;
6805   int tmp___0 ;
6806
6807   {
6808   tmp = request_irq(irq, handler, flags, name, dev);
6809   ldv_func_res = tmp;
6810   tmp___0 = reg_check_1(handler);
6811   if (tmp___0 != 0 && ldv_func_res == 0) {
6812     activate_suitable_irq_1((int )irq, dev);
6813   } else {
6814
6815   }
6816   return (ldv_func_res);
6817 }
6818 }
6819 void ldv_free_irq_20(unsigned int ldv_func_arg1 , void *ldv_func_arg2 )
6820 {
6821
6822
6823   {
6824   free_irq(ldv_func_arg1, ldv_func_arg2);
6825   disable_suitable_irq_1((int )ldv_func_arg1, ldv_func_arg2);
6826   return;
6827 }
6828 }
6829 __inline static void ldv_error(void);
6830 int ldv_spin =    0;
6831 void ldv_check_alloc_flags(gfp_t flags )
6832 {
6833
6834
6835   {
6836   if (ldv_spin == 0 || ! (flags & 16U)) {
6837
6838   } else {
6839     ldv_error();
6840   }
6841   return;
6842 }
6843 }
6844 extern struct page___0 *ldv_some_page(void) ;
6845 struct page___0 *ldv_check_alloc_flags_and_return_some_page(gfp_t flags )
6846 {
6847   struct page___0 *tmp ;
6848
6849   {
6850   if (ldv_spin == 0 || ! (flags & 16U)) {
6851
6852   } else {
6853     ldv_error();
```

Warning: some elements are missing

1 No call stack (please add tags "enterFunction" and "returnFrom" to improve visualization)
2 No conditions (please add tags "control" to improve visualization)

Witness was produced by ESBMC-kind tool.

Witness Visualizer Application Area

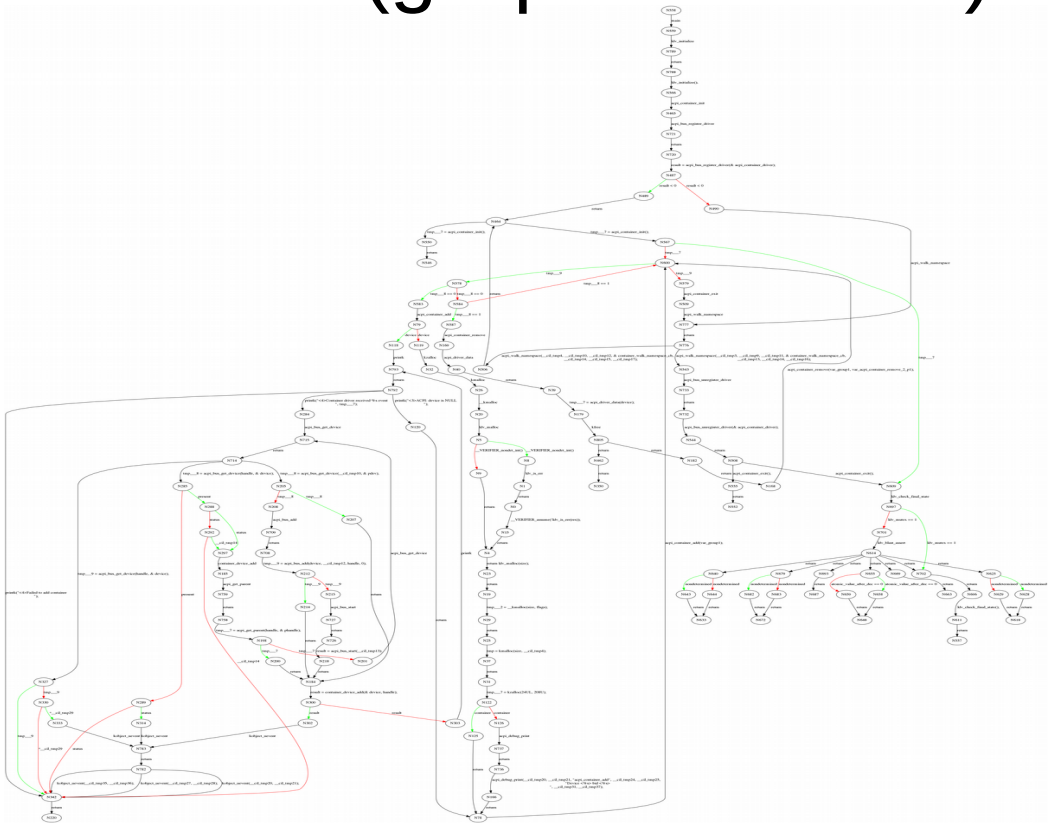
- Demonstration of a generic witness
  - Supports any SV-COMP tool
- Feedback to the developers
  - Missing elements, warnings, etc.
- Large witnesses visualization
  - Based on extracted violation hints
- Comparison of witnesses
  - Required for several witnesses

Suggested Solutions

- Witness Visualizer (user-friendly witnesses)
  - Helps to locate bugs for the users
  - Helps to reveal problems in tools
- Correctness witnesses visualization (idea)
  - Shows main proof hints (for developers)
  - Presents source code coverage (for users)
- Benchmark Visualizer (continuous verification)
  - Visualizes BenchExec results
  - Groups witnesses for each benchmark

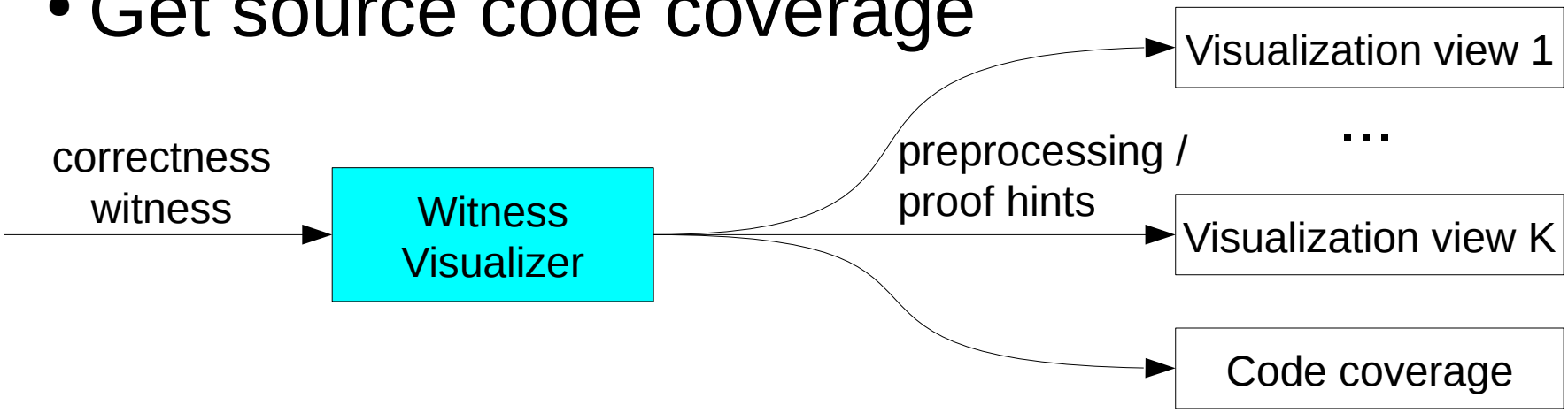
Correctness Witnesses

- Present main verification result (proof)
  - Ensure the absence of missed bugs
- Hard to visualize (graph structure)



General Ideas of the Visualization

- Support of common format* (GraphML)
- Witness preprocessing
  - Convert to the plain structure
- Extract main proof hints
  - Conditions, invariants, etc.
- Get source code coverage

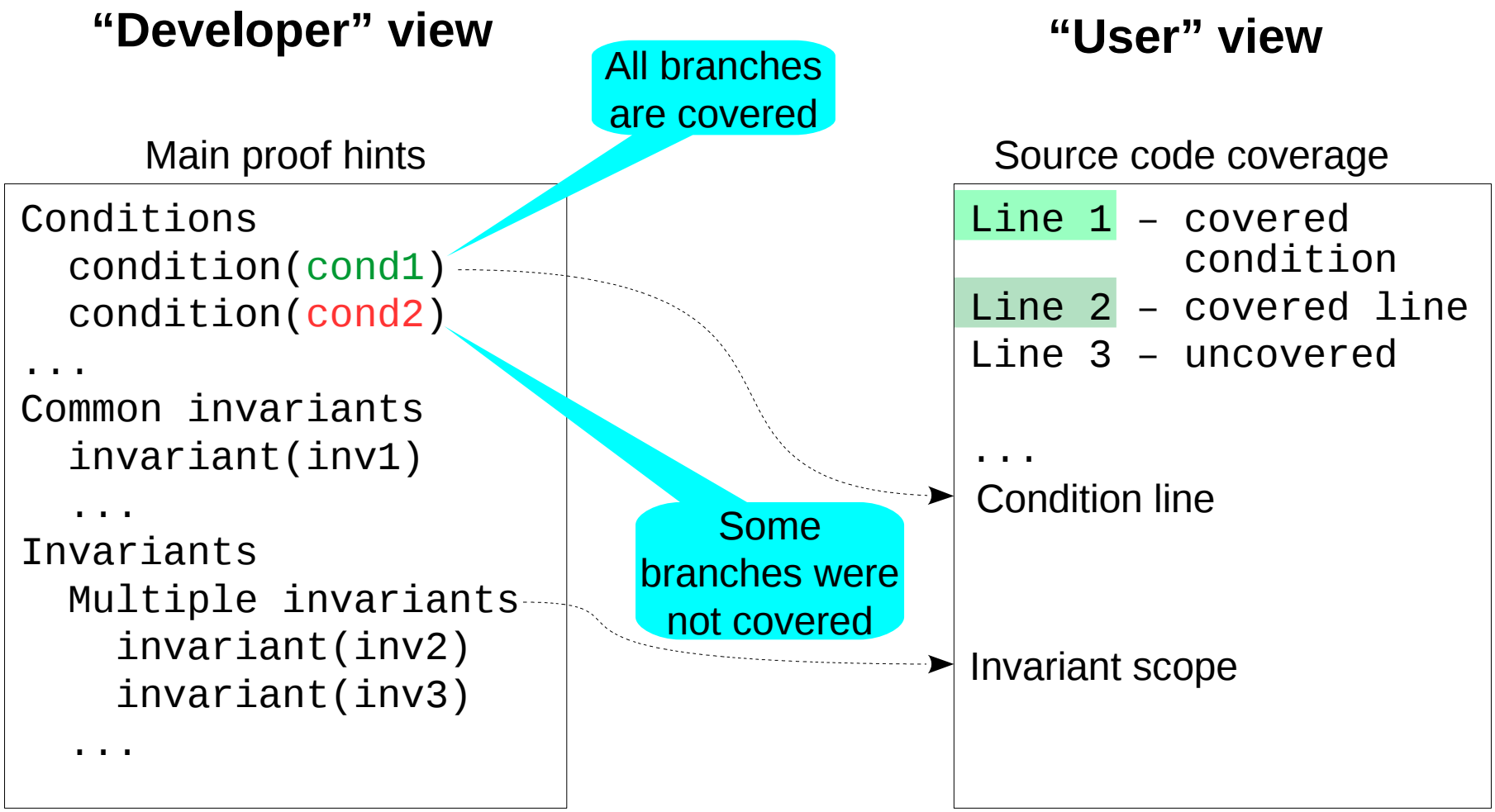


* D. Beyer, M. Dangl, D. Dietsch, M. Heizmann. *Correctness witnesses: exchanging verification results between verifiers*. ACM, 2016.

Implementation of the Suggested Ideas

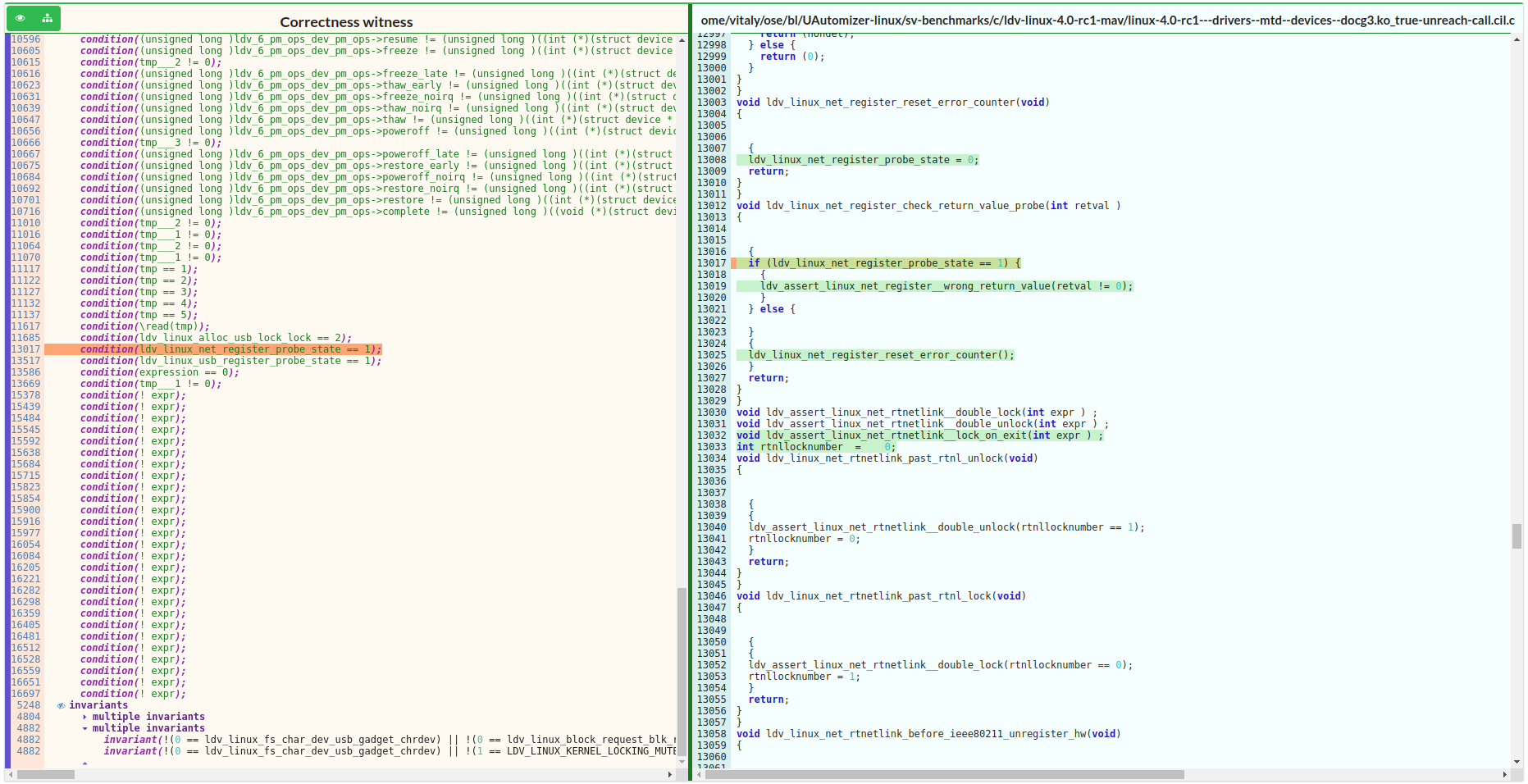
- Proof hints
  - Conditions
  - Invariants (common and local)
- Witness preprocessing
  - Sort all elements by line/thread/source file
  - Combine all assumptions for conditions
  - Extract common invariants
- Witness comparison
  - Is not supported (only 1 (?) witness is expected)

Correctness Witness Model Example



Correctness Witness Example

UAutomizer correctness witness visualization*



* Sometimes SV-COMP tools may produce empty correctness witnesses.

Correctness Witnesses Visualization

- Suggested general ideas of the visualization
  - Based on plain structure and proof hints
- Suggested implementation of the ideas
  - Based on conditions and invariants
  - Other implementations can be suggested
- Idea to extract source code coverage
- Can be useful for both developers and users

Suggested Solutions

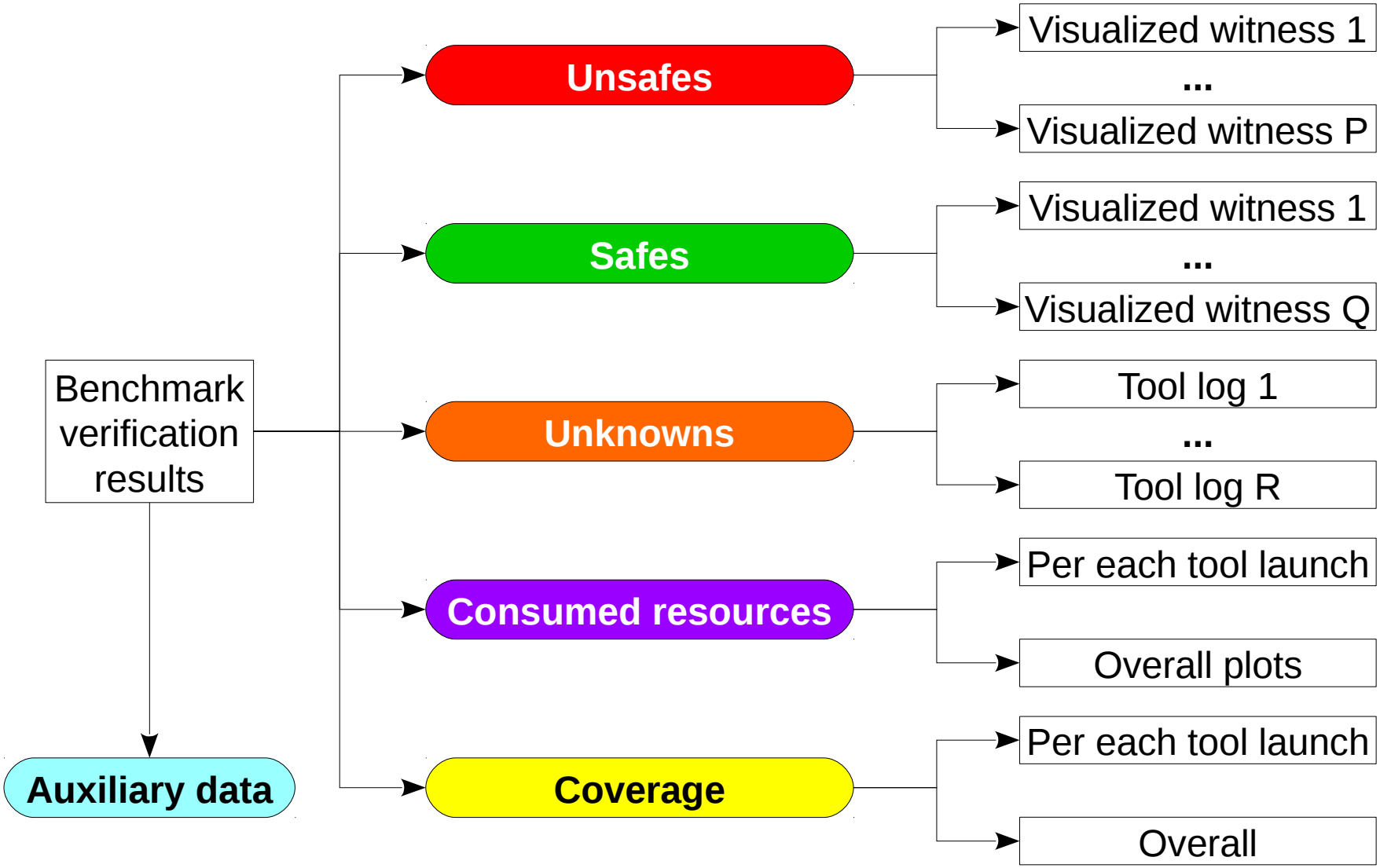
- Witness Visualizer (user-friendly witnesses)
  - Helps to locate bugs for the users
  - Helps to reveal problems in tools
- Correctness witnesses visualization (idea)
  - Shows main proof hints (for developers)
  - Presents source code coverage (for users)
- Benchmark Visualizer (continuous verification)
  - Visualizes BenchExec results
  - Groups witnesses for each benchmark

Benchmark Visualizer*

- Web-interface** for verification results visualization
  - Easy to setup and use
- Database
  - Benchmark verification results
  - User marks: bugs, incorrect proofs, etc.
- Differences with BenchExec table-generator
  - Witnesses in user-friendly format
  - Means for manual results analysis
  - Coverage (if presented)

* https://github.com/ispras/cv
** https://github.com/mutilin/klever (branch cv-v2.0)

Benchmark Verification Results Visualization



Benchmark Visualizer Database

Data base

Compare

Benchmark verification results 1

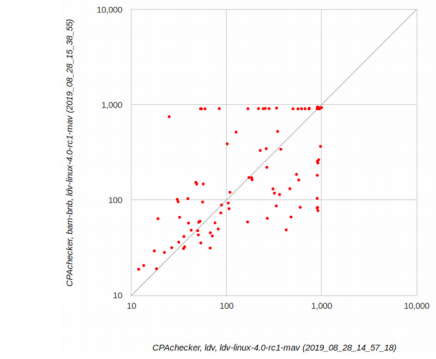
...

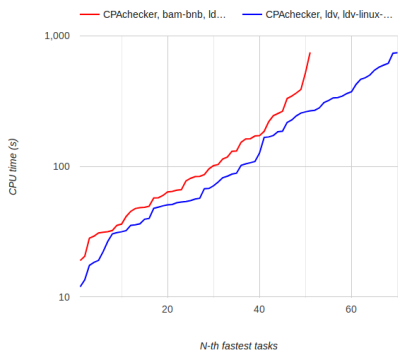
Benchmark verification results T

Witnesses marks

Results transitions	Witness clusters
Safe → Unknown: 2 Unsafe → Unknown: 25 Unknown → Safe: 6 Unknown → Unsafe: 2	Common: 23 Lost: 27 New: 24

Consumed resources





Benchmark verification results T+1

Add new results

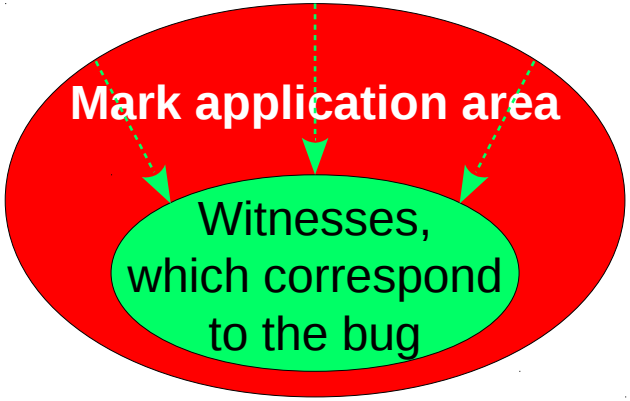
Compare new witnesses with already marked ones

Speedups manual analysis

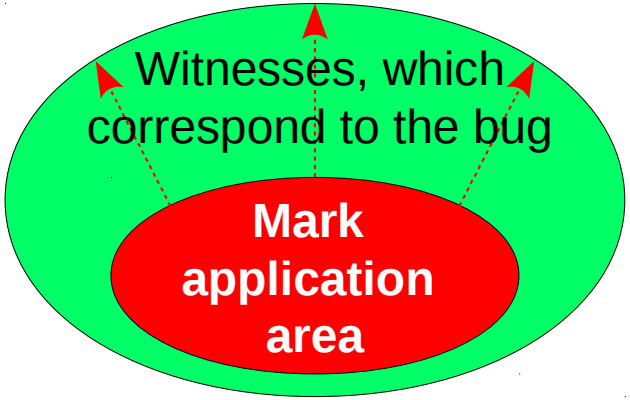
Manual Results Analysis*

- User analyses a witness to determine a bug
- User creates a mark for a bug
  - Witness + comparison criteria + description
- User adjusts the created mark

The mark is applied to a witness, which corresponds to other bugs



There is a witness, which corresponds to the same bug, but the mark was not applied to it



* Based on violation witnesses.

Benchmark Visualizer Example

Violation witnesses

Correctness witnesses

Verifier logs

Unsafes: 59

- Details
  - Bugs: 1
  - False positives: 13
    - Rule: 5
    - EnvironmentModel: 8
  - Unknown: 3
  - Without marks: 42

Safes: 11

- Details
  - Unknown: 7
    - Low coverage: 7
  - Without marks: 4

Unknowns: 28

- Components
  - CPAchecker: 28
    - Assertion: 19
    - Parsing: 1
    - Soft time limit: 23
    - Time limit: 23

Coverage: 56.61% by functions / 20.81% by lines

- Details

Coverage type	Function coverage	Line coverage
Covered by all properties	56.61%	20.81%
Property 'unreach-call'	56.61%	20.81%

Coverage per property

Consumed resources: 35 835 s / 15 GB

- Details

Component	CPU time	Memory	Wall time
CPAchecker	35 789 s	15 GB	8.1 h
Coverage	8 s	52 MB	16 s
Exporter	0 s	21 MB	290 ms
Launcher	1 s	20 MB	15 s
MEA	36 s	91 MB	37 s
Overall	35 835 s	15 GB	15 s

Consumed resources plots

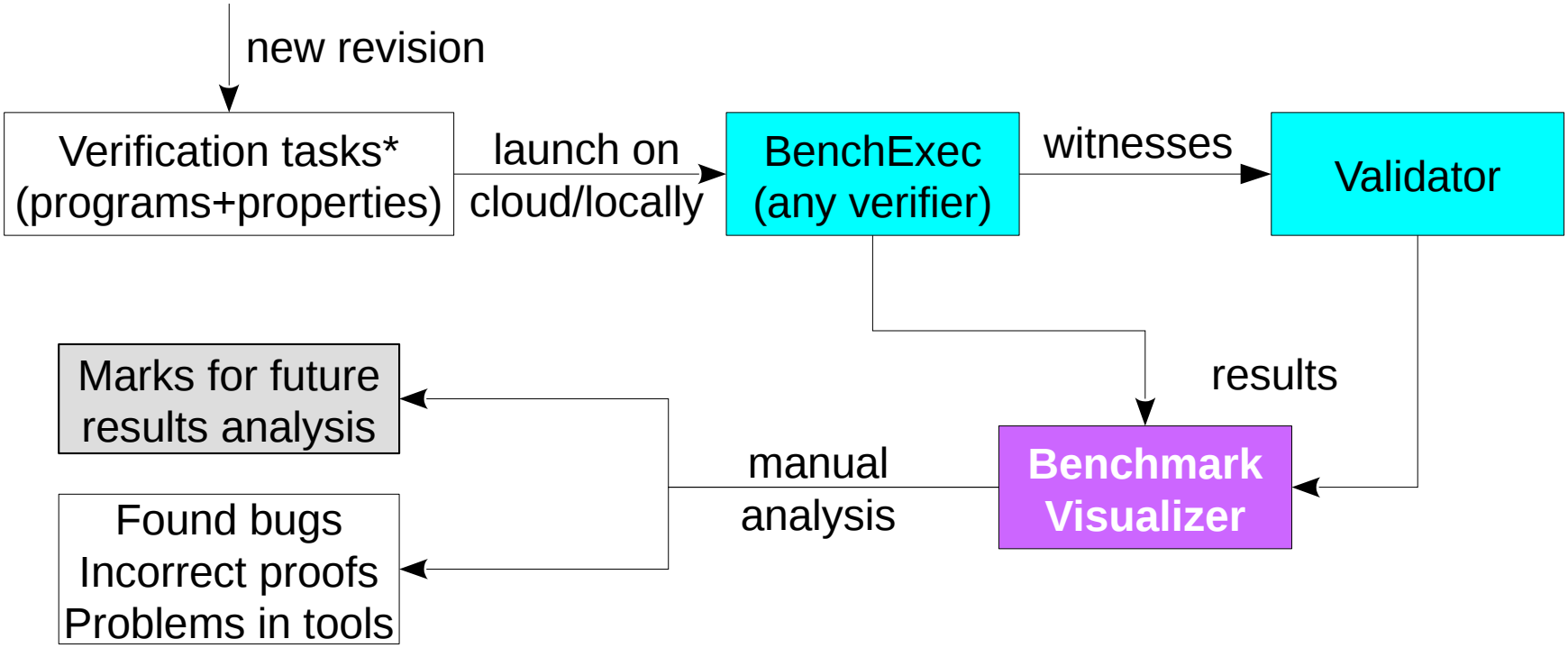
CPAchecker, ldv, ldv-linux-4.0-rc1-mav (2019_08_28_14_57_18)

Description	SV-COMP benchmarks results
Last change	2 weeks, 5 days ago (uploader uploader)
Status	Solved (components tree)
Attributes filters	Apply
Rule specification	unreach-call
Verification object	98 elements
Configuration	
CPU cores limit	2
CPU time limit	900s
Memory limit	16000000000
Options	Show

Auxiliary data / filters

Benchmark Visualizer Application Area

- Continuous verification of industry systems



- SV-COMP tasks
  - Similar work-flow with the given verification tasks

* May require additional preparation by the user or other tool.

Conclusion

- Witness Visualizer
  - Converts witnesses to user-friendly format
- Correctness witnesses visualization
  - New ideas of visualization
  - A simple implementation of the ideas
- Benchmark Visualizer
  - Successfully applied to continuous verification
  - Can be applied for SV-COMP tasks

Future Plans

- Witness Visualizer improvements
  - Restore missing elements where possible
  - Improve feedback to the developers
- Correctness witnesses visualization
  - Suggest other views based on proof hints
  - Implement automatic coverage extraction
- Benchmark Visualizer improvements
  - Regression verification
  - Verification tasks preparation

The joint 4rd International Workshop on CPAchecker (CPA'19) and
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October 2, 2019, Frauenchiemsee, Germany

Thank you

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